



Patent  
706316-1208

**RESPONSE UNDER 37 C.F.R. §1.116**  
--EXPEDITED PROCEDURE--  
EXAMINING GROUP 2123

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

) Confirmation No. 9321

Inventor: Beausoleil et al.

)

Serial No.: 09/655,595

)

Filed: September 6, 2000

)

Group Art Unit: 2123

)

Examiner: Thomas H. Stevens

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For: High Speed Software Driven Emulator  
Comprised of a Plurality of Emulation  
Processors with Improved Board-to-Board  
Interconnection Cable Length Identification  
System

I hereby certify that this correspondence is being  
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Sir:

This paper is responsive to the Final Office Action mailed on May 16, 2005.

**Recitation of the pending Claims** are reflected in the listing of claims, which begins on  
page 2 of this paper.

**Remarks/Arguments** begin on page 4 of this paper.